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CRR SERIES DIGITAL PLUG-IN TIME DELAY RELAY

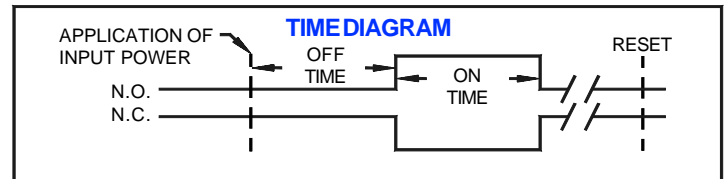


UL
 UL/cUL Recognized

File E80165
 UL Guide NKCR2
 cUL Guide NKCR8

OFF/ON/RECYCLE

Upon application of power to the input terminals, the **OFF** delay begins. Upon completion of the **OFF** delay, the output contacts transfer and the **ON** delay begins. Upon completion of the **ON** delay, the output contacts revert to their original position and the cycle repeats. Reset is accomplished by removal of input power.



FEATURES

- C/MOS Digital Circuitry
- Time Delays To 1000 Minutes
- No First Cycle Effect
- 0.5% Repeat Accuracy
- 2% Stability Over Voltage And Temperature
- Wide Voltage Selection 24-230 VAC, 12 - 110 VDC

SPECIFICATIONS

1. Time Delay.

- 1.1 Type: C/MOS Digital Circuitry
- 1.2 Range: From 0.05 seconds to 1000 minutes. Fixed delays available (see *time delay range chart*)
- 1.3 Repeat accuracy: $\pm 0.5\%$ under fixed conditions
- 1.4 Setting accuracy: $\pm 10\%$
- 1.5 Reset time: 100 milliseconds maximum
- 1.6 Recycle time: 150 milliseconds
- 1.7 Time delay vs. voltage and temperature: $\pm 2\%$

2. Input.

- 2.1 Operating voltage: 24, 120 & 230 VAC, 12, 24 & 110 VDC
- 2.2 Tolerance: $\pm 20\%$ of nominal
- 2.3 Frequency: 50 - 60 Hertz

3. Output.

- 3.1 Type: Electromechanical relay
- 3.2 Form: DPDT
- 3.3 Rating: 10 amperes resistive @ 30 VDC, 120/240 VAC
- 3.4 Life: Electrical - full load - 1,000,000 operations
 Mechanical - 10,000,000 operations

4. Protection.

- 4.1 Transient: ± 1500 volts for 150 microseconds
- 4.2 Polarity: DC units are reverse polarity protected
- 4.3 Dielectric breakdown: 1500 volts RMS minimum

5. Mechanical.

- 5.1 Mounting: Plug-in
- 5.2 Termination: Octal (8 pin), Magnal (11 pin) or 11 pin stab/square base plug-in

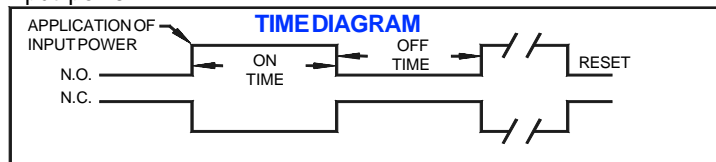
6. Environmental.

- 6.1 Operating temperature: -20°C to $+80^{\circ}\text{C}$
- 6.2 Storage temperature: -30°C to $+85^{\circ}\text{C}$
- 6.3 Humidity: 95% relative, non-condensing

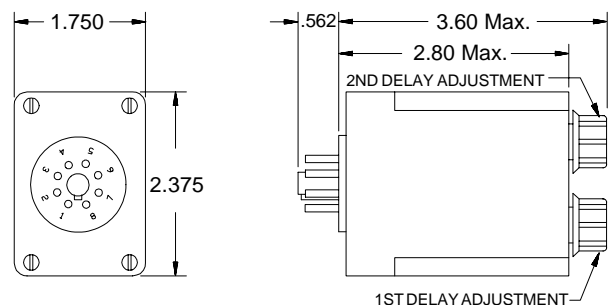
MODE OF OPERATION

ON/OFF/RECYCLE

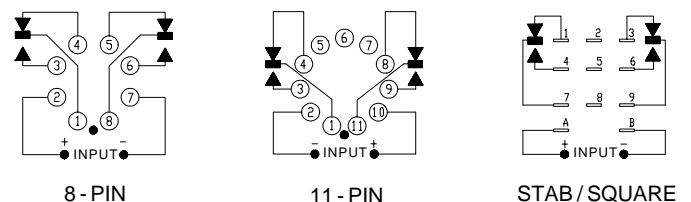
Upon application of power to the input terminals, the **ON** delay begins and the output contacts transfer. Upon completion of the **ON** delay, the output contacts revert back to their original position and the **OFF** delay begins. Upon completion of the **OFF** delay, the output contacts again transfer and the cycle repeats. Reset is accomplished by removal of input power.



DIMENSIONS



CONNECTION DIAGRAMS



ORDERING INFORMATION

SERIES	BASE STYLE	INPUT VOLTAGE	ADJUSTMENT	CYCLE	1ST TIME RANGE	2ND TIME RANGE
CRR	1 - Octal Plug-In (8 Pin)	1 - 12 VDC	0 - Knob	1 - On Time First	See Time Delay Range Chart	
	2 - 11 Pin Plug-In	2 - 24/28 VDC	1 - Fixed	2 - Off Time First		
	3 - 11 Pin	3 - 110 VDC				
	4 - 24 VAC	4 - 24 VAC				
	5 - 120 VAC	5 - 120 VAC				
	6 - 230 VAC	6 - 230 VAC				