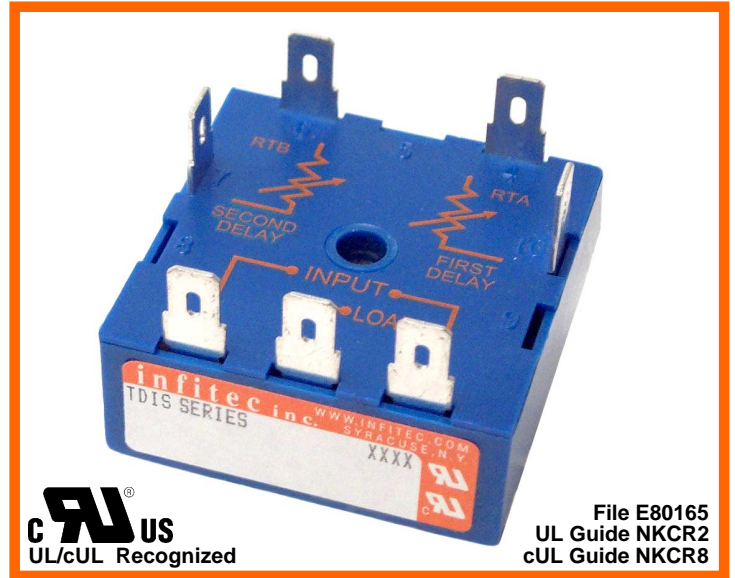




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TDIS SERIES DELAYED INTERVAL TIME DELAY MODULE



UL
 UL/cUL Recognized

File E80165
 UL Guide NKCR2
 cUL Guide NKCR8

FEATURES

- C/MOS Digital Circuitry
- Independent External Timing Adjustments
- Delays From 0.05 Seconds To 1000 Minutes
- Fully Solid State And Encapsulated
- No First Cycle Effect
- 0.5% Repeat Accuracy
- Low Cost Mounting And Termination
- Output Rated 1 Ampere Continuous, 10 Amperes Inrush

SPECIFICATIONS

1. Time Delay.

- 1.1 Type: C/MOS digital circuitry
- 1.2 Range: From 0.05 seconds to 1000 minutes. Fixed delays available (see time delay range chart)
- 1.3 Repeat accuracy: $\pm 0.5\%$ under fixed conditions
- 1.4 Setting accuracy: $\pm 10\%$
- 1.5 Reset time: 100 milliseconds maximum
- 1.6 Recycle time: 150 milliseconds
- 1.7 Time delay vs. voltage and temperature: $\pm 2\%$

2. Input.

- 2.1 Operating voltage: 24, 120 & 230 VAC, 12 & 24/28 VDC
- 2.2 Tolerance: $\pm 20\%$ of nominal
- 2.3 Frequency: 50 - 60 Hertz

3. Output.

- 3.1 Type: Solid state
- 3.2 Form: SPST, N.O.
- 3.3 Rating: 1 ampere maximum (20mA minimum or consult factory) - 10 amperes inrush
- 3.4 Life: 100,000,000 operations minimum under full load

4. Protection.

- 4.1 Transient: ± 1500 volts for 150 microseconds
- 4.2 Polarity: DC units are reverse polarity protected
- 4.3 Dielectric breakdown: 1500 volts RMS minimum

5. Mechanical.

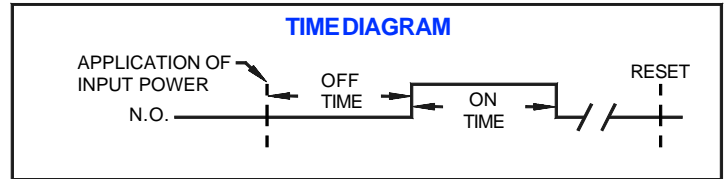
- 5.1 Mounting: One #8 or #10 screw
- 5.2 Termination: 1/4" quick connect terminals
- 5.3 Style: Surface mount / encapsulated

6. Environmental.

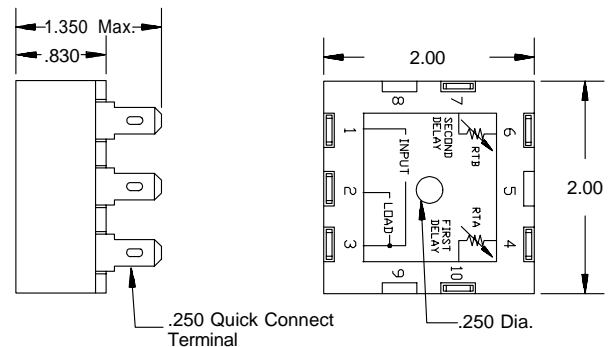
- 6.1 Operating temperature: -20°C to $+80^{\circ}\text{C}$
- 6.2 Storage temperature: -30°C to $+85^{\circ}\text{C}$
- 6.3 Humidity: 95% relative, non-condensing

MODE OF OPERATION DELAYED INTERVAL

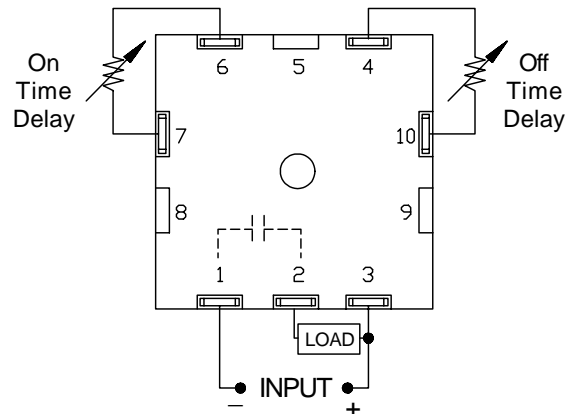
Upon application of power to the input terminals, the **OFF** delay begins. Upon completion of the **OFF** delay, the output contact transfers and the **ON** delay begins. Upon completion of the **ON** delay, the output contact reverts back to its original position. Reset is accomplished by removal of input power.



DIMENSIONS



CONNECTION DIAGRAM



ORDERING INFORMATION

SERIES	INPUT VOLTAGE	ADJUSTMENT	OFF TIME	ON TIME
TDIS	1 - 12 VDC 2 - 24/28 VDC 4 - 24 VAC 5 - 120 VAC 6 - 230 VAC	1 - Both Delays Factory Fixed 1A - 1st Delay Fixed 2nd Delay Adj. 1B - 1st Delay Adj. 2nd Delay Fixed 2 - Both Delays External Adj.	See Time Delay Range Chart	