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QSC SERIES DIGITAL ENCAPSULATED TIME DELAY MODULES

FEATURES

- C/MOS Digital Circuitry
- Time Delays To 1000 Minutes
- No First Cycle Effect
- Fully Solid State And Encapsulated
- 0.5% Repeat Accuracy
- Seven Different Modes Of Operation
- Output Rated at 1 Ampere Continuous, 10 Amperes Inrush
- Fixed Or Adjustable Time Delays
- Small Size
- UL/cUL Recognized



UL
 UL/cUL Recognized

File E80165
 UL Guide NKCR2
 cUL Guide NKCR8

SPECIFICATIONS

1. Time Delay.

- 1.1 Type: C/MOS digital circuitry
- 1.2 Range: From 0.05 seconds to 1000 minutes. Fixed delays available (*see time delay range chart*)
- 1.3 Repeat accuracy: $\pm 0.5\%$ under fixed conditions
- 1.4 Setting accuracy: $\pm 10\%$
- 1.5 Reset time: 50 milliseconds maximum
- 1.6 Recycle time: 100 milliseconds during timing, 50 milliseconds after timing
- 1.7 Time delay vs. voltage and temperature: $\pm 2\%$

2. Input.

- 2.1 Operating voltage: 24, 120 & 230 VAC, 12 & 24/28 VDC
- 2.2 Tolerance: $\pm 20\%$ of nominal
- 2.3 Frequency: 50 - 60 Hertz

3. Output.

- 3.1 Type: Solid state
- 3.2 Form: SPST
- 3.3 Rating: 1 amp steady state, (10 amp inrush, 20 mA. min.)
- 3.4 Life: 100,000,000 operations minimum under full load

4. Protection.

- 4.1 Transient: ± 1500 volts for 150 microseconds
- 4.2 Polarity: DC units are reverse polarity protected
- 4.3 Dielectric breakdown: 1500 volts RMS minimum

5. Mechanical.

- 5.1 Mounting: Via 1/2 inch conduit nipple/locknut
- 5.2 Termination: 6 inch (min.), #20 AWG, wire leads
- 5.3 Style: Encapsulated 2" x 3"

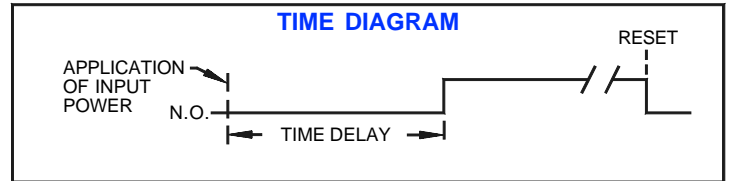
6. Environmental.

- 6.1 Operating temperature: -20°C to $+80^{\circ}\text{C}$
- 6.2 Storage temperature: -30°C to $+85^{\circ}\text{C}$
- 6.3 Humidity: 95% relative non-condensing

MODE OF OPERATION - SERIES

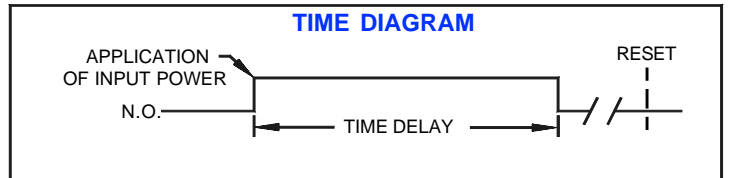
DELAY ON MAKE - QMSC

Upon application of power to the input terminals, the time delay begins. At the completion of the pre-selected time delay, the output contact transfers. Reset is accomplished by removal of input power. There is no false output when reset during timing.



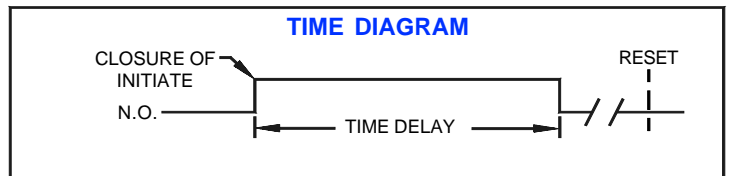
INTERVAL - QISC

Upon application of power to the input terminals, the output contact immediately transfers and the time delay begins. At the completion of the pre-selected time delay, the output contact reverts to its original position. Reset is accomplished by removal of input power.



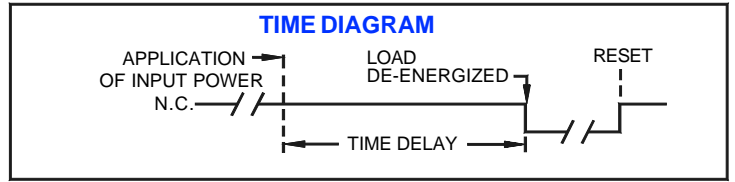
SINGLE SHOT - QSSC

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch (momentary or maintained) the output contact transfers and the time delay begins. At the completion of the pre-selected delay period, the output contact reverts to its original position. Removal of input power will reset the control.



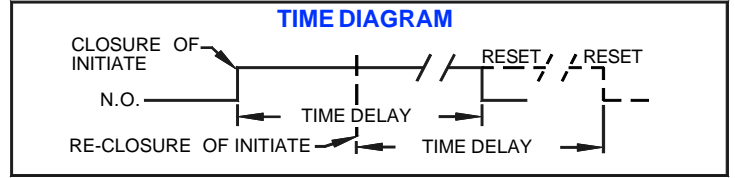
DELAY ON MAKE, NORMALLY CLOSED - QCSC

The output is in a normally closed state. Upon application of power to the input terminals, the output contact transfers and the time delay begins. At the completion of the time delay the output contact drops out. Removal of input power from terminal 3 resets the delay and the output contact reverts to its original closed position.



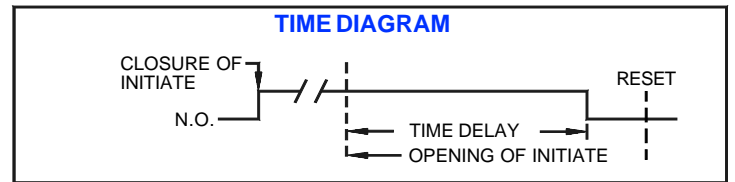
RETRIGGERABLE ONE-SHOT - QOSC

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch (momentary or maintained) the output contact transfers and the time delay begins. At the completion of the pre-selected delay period, the output contact reverts to its original position. NOTE: Momentary or maintained closure of initiate switch during timing will reset the time delay.



DELAY ON BREAK - QBSC/QTSC *

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch, the output contact transfers and remains transferred if no further action is taken. When the initiate switch is opened, the time delay begins. At the completion of the pre-selected delay period the output contact reverts to its original position. Closure of initiate during timing will reset the delay period. Removal of input power will reset the control.



* QTSC is the same except it is trailing edge triggered. Load energizes when initiate switch is opened.

DIMENSIONS

CONNECTION DIAGRAMS

ORDERING INFORMATION

SERIES	INPUT VOLTAGE	ADJUSTMENT	TIME DELAY RANGE
QBSC	1 - 12 VDC	1 - Fixed	See Time Delay Range Chart
QCSC	2 - 24/28 VDC	2 - External Adjust	
QISC	4 - 24 VAC		
QMSC	5 - 120 VAC		
QOSC	6 - 230 VAC		
QSSC			
QTSC			