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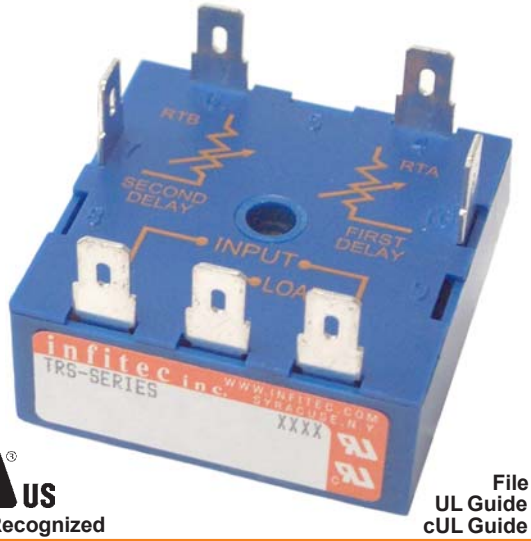
TRS SERIES
DIGITAL ENCAPSULATED REPEAT CYCLE TIME DELAY MODULE



c UL us
UL/cUL Recognized

File E80165
UL Guide NKCR2
cUL Guide NKCR8

FEATURES

- C/MOS Digital Circuitry
- Fixed, Independent Local or External Timing Adjustments
- Time Delays To 1000 Minutes
- Fully Solid State And Encapsulated
- No First Cycle Effect
- 0.5% Repeat Accuracy
- Low Cost Mounting And Termination
- Output Rated 1 Amp Continuous - 10 Amp Inrush
- UL/cUL Recognized

SPECIFICATIONS

1. Time Delay.
1.1 Type: C/MOS digital circuitry
1.2 Range: From 0.05 seconds to 1000 minutes.
Fixed delays available
1.3 Repeat accuracy: ± 0.5% under fixed conditions
1.4 Setting accuracy: ± 10%
1.5 Reset time: 100 milliseconds maximum
1.6 Recycle time: 150 milliseconds
1.7 Time delay vs. voltage and temperature: ± 2%

2. Input.
2.1 Operating voltage: 24, 120 & 230 VAC, 12, 24/28 & 36 VDC
2.2 Tolerance: ± 20% of nominal
2.3 Frequency: 50 - 60 Hertz

3. Output.
3.1 Type: Solid state
3.2 Form: SPST
3.3 Rating:1 amp steady state,*(10 amp inrush,20 mA. min.)*
3.4 Life: 100,000,000 operations minimum under full load

4. Protection.
4.1 Transient: ± 1500 volts for 150 microseconds
4.2 Polarity: DC units are reverse polarity protected
4.3 Dielectric breakdown: 1500 volts RMS minimum

5. Mechanical.
5.1 Mounting: One #8 or #10 screw
5.2 Termination: 1/4" quick connect terminals
5.3 Style: Surface mount encapsulated

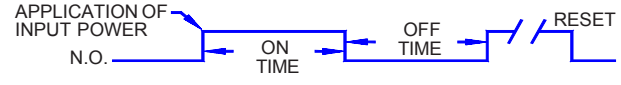
6. Environmental.
6.1 Operating temperature: -20°C to +80°C
6.2 Storage temperature: -30°C to +85°C
6.3 Humidity: 95% relative non-condensing

MODE OF OPERATION

On/Off Recycle

Upon application of power to the input terminals, the **ON** delay begins and the output contact transfers. Upon completion of the **ON** delay, the output contact reverts back to its original position and the **OFF** delay begins. Upon completion of the **OFF** delay, the output contact again transfer and the cycle repeats. Reset is accomplished by removal of input power.

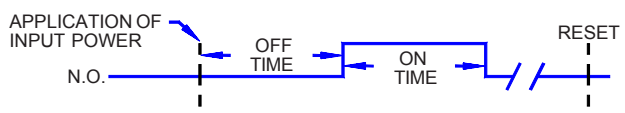
TIME DIAGRAM



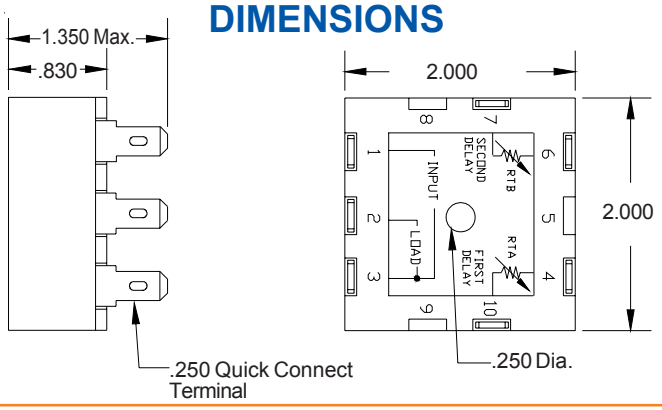
Off/On Recycle

Upon application of power to the input terminals, the **OFF** delay begins. Upon completion of the **OFF** delay, the output contact transfers and the **ON** delay begins. Upon completion of the **ON** delay, the output contact reverts back to its original position and the cycle repeats. Reset is accomplished by removal of input power.

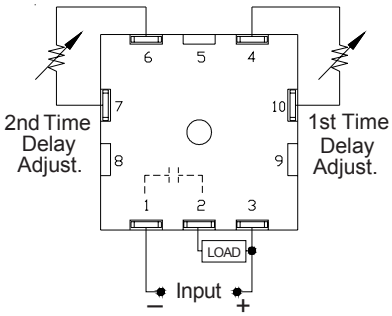
TIME DIAGRAM



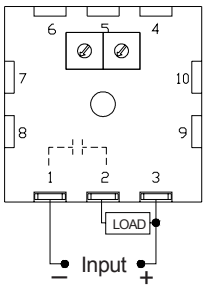
DIMENSIONS



CONNECTION DIAGRAMS



External Adjustments Shown



Local Adjustments Shown

ORDERING INFORMATION

SERIES	INPUT VOLTAGE	ADJUSTMENT	CYCLE	1ST TIME RANGE	2ND TIME RANGE
TRS	**1 -** 12 VDC **2 -** 24/28 VDC **4 -** 24 VAC **5 -** 120 VAC **6 -** 230 VAC **9 -** 36 VDC	**0 -** Both Delays Local Adj. **0A-** 1st Delay Fixed 2nd Delay Local Adj. **0B-** 1st Delay Local Adj. 2nd Delay Fixed **0C-** 1st Delay Ext. Adj. 2nd Delay Local Adj. **0D-** 1st Delay Local Adj. 2nd Delay Ext. Adj. **1 -** Both Delays Factory Fixed **1A-** 1st Delay Fixed 2nd Delay Ext. Adj. **1B-** 1st Delay Ext. Adj. 2nd Delay Fixed **2 -** Both Delays Ext. Adj.	**1 -** On Time First **2 -** Off Time First	See Time Delay Range Chart	